

Abstract of the Disclosure

Disclosed is an internal voltage generator which generates a stable internal voltage using two power up sensing means.

5 Clamp means outputs a first voltage. First and second power up sensing means sense the external applied to the semiconductor device and output first and second control signals, respectively. A first switch receives the first voltage and a switch controller receives the first and second control signals from the first and second power up sensing means and controls turn on/off of the first switch. A second switch is turned on/off according to the second control signal from the second power up sensing means and receives a second voltage. An amplifier selectively receives the first and second voltages from the first and second switches and outputs the second voltage.

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